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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/716,278	11/18/2003	Nace Layadi	LAYADI 36-39	6063
47396	7590 11/04/2005		EXAM	INER
HITT GAINES, PC AGERE SYSTEMS INC.			WILSON, CHRISTIAN D	
PO BOX 832570			ART UNIT	PAPER NUMBER
RICHARDSON, TX 75083			2891	
•	·		DATE MAILED: 11/04/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	10/716,278	LAYADI ET AL.				
Office Action Summary	Examiner	Art Unit				
	Christian Wilson	2891				
The MAILING DATE of this communicati Period for Reply	on appears on the cover sheet wi	th the correspondence address				
A SHORTENED STATUTORY PERIOD FOR THE MAILING DATE OF THIS COMMUNICAT  - Extensions of time may be available under the provisions of 37 after SIX (6) MONTHS from the mailing date of this communica  - If the period for reply specified above is less than thirty (30) day  - If NO period for reply is specified above, the maximum statutory  - Failure to reply within the set or extended period for reply will, b  - Any reply received by the Office later than three months after the  - earned patent term adjustment. See 37 CFR 1.704(b).	TION.  CFR 1.136(a). In no event, however, may a retion.  s, a reply within the statutory minimum of thirty period will apply and will expire SIX (6) MON y statute, cause the application to become AB	eply be timely filed  y (30) days will be considered timely.  THS from the mailing date of this communication.  ANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed or	1 <u>9 August 2005</u> .					
2a)⊠ This action is FINAL. 2b)□	This action is non-final.					
,						
Disposition of Claims						
4) ⊠ Claim(s) 1-18 is/are pending in the applie 4a) Of the above claim(s) is/are w 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 1-18 is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction	ithdrawn from consideration.					
Application Papers						
9)☐ The specification is objected to by the Ex 10)☑ The drawing(s) filed on 18 November 200 Applicant may not request that any objection Replacement drawing sheet(s) including the 6 11)☐ The oath or declaration is objected to by	23 is/are: a)⊠ accepted or b)□ to the drawing(s) be held in abeyan correction is required if the drawing(	ce. See 37 CFR 1.85(a). s) is objected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for for a) All b) Some * c) None of:  1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International E * See the attached detailed Office action for	uments have been received.  uments have been received in Ape priority documents have been  Bureau (PCT Rule 17.2(a)).	pplication No received in this National Stage				
Attachment(s)						
1)		ummary (PTO-413) )/Mail Date				
<ul> <li>Review (FTO-size)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/Paper No(s)/Mail Date</li> </ul>		formal Patent Application (PTO-152)				

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## **DETAILED ACTION**

# Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the first paragraph of 35 U.S.C. 112:
  - The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.
- 2. Claims 1 18 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claims contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventors, at the time the application was filed, had possession of the claimed invention. The addition to claim 1 and 10 of the limitation "photoresist" is new matter since it was not described in the application at the time of filing.

## Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claims 1 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Witek *et al.* in view of Gopalan *et al.*

Regarding claim 1, Witek et al. (US 6,146,970) teaches a method of manufacturing a trench isolation structure comprising the steps of forming a polysilicon hardmask 206 over a

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substrate 202, forming a photoresist layer 208, forming an opening in the hardmask with the photoresist [Figure 6], etching a trench 210 in the substrate, and filling the trench with an insulative material 216a. Witek *et al.* does not discuss removing the photoresist before etching the trench. Gopalan *et al.* (US 6,794,269) teaches removing the photoresist before etching the trench in the substrate [column 8, lines 1-15]. It would have been obvious to one of ordinary skill in the art to remove the photoresist in Witek *et al.* before forming the trench since Gopalan *et al.* teaches that a two step etching process with removal before etching steps provides more etch selectivity to substrate than the hardmask layer.

Regarding claims 2 and 3, Witek *et al.* further teaches a pad oxide **204** with a thickness of 5 to 25 nm [column 6, line 16].

Regarding claims 4 and 5, Witek *et al.* further teaches a liner oxide **212** with a thickness of 10 to 50 nm [column 7, line 6].

Regarding claim 6, Witek et al. further teaches depositing the insulative material within the trench [Figure 8].

Regarding claim 7, Witek *et al.* further teaches a polysilicon hardmask with a thickness of 80 to 200 nm [column 6, line 22].

Regarding claim 9, Witek et al. further teaches a trench isolation structure [Figure 9].

Regarding claim 10, Witek *et al.* teaches a method of manufacturing a trench isolation structure comprising the steps of forming a polysilicon hardmask **206** over a substrate **202**, forming a photoresist layer **208**, forming an opening in the hardmask with the photoresist [Figure 6], etching a trench **210** in the substrate, filling the trench with an insulative material **216a**, forming transistor devices **230** over the substrate, and constructing an interlevel dielectric layer

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262 over the devices and having interconnects contacting the devices [column 2, lines 20-25]. Witek *et al.* does not discuss removing the photoresist before etching the trench. Gopalan *et al.* teaches removing the photoresist before etching the trench in the substrate [column 8, lines 1-15]. It would have been obvious to one of ordinary skill in the art to remove the photoresist in Witek *et al.* before forming the trench since Gopalan *et al.* teaches that a two step etching process with removal before etching steps provides more etch selectivity to substrate than the hardmask layer.

Regarding claims 11 and 12, Witek et al. further teaches a pad oxide 204 with a thickness of 5 to 25 nm [column 6, line 16].

Regarding claims 13 and 14, Witek et al. further teaches a liner oxide 212 with a thickness of 10 to 50 nm [column 7, line 6].

Regarding claim 15, Witek *et al.* further teaches depositing the insulative material within the trench [Figure 8].

Regarding claim 16, Witek *et al.* further teaches a polysilicon hardmask with a thickness of 80 to 200 nm [column 6, line 22].

Regarding claim 18, Witek et al. further teaches a trench isolation structure [Figure 9].

Regarding claims 8 and 17, Witek *et al.* teaches a trench with a depth of 0.3 to 0.7 µm [column 6, line 50], but does not discuss the width of the trenches. Gopalan *et al.* teaches a trench width of 1 to 5 µm [column 7, line 42]. It would have been obvious to one of ordinary skill in the art to use the trench widths of Gopalan *et al.* in the method of Witek *et al.* since these widths provide reduced substrate capacitance of the circuit devices.

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#### Conclusion

5. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christian Wilson whose telephone number is (571) 272-1886. The examiner can normally be reached on weekdays, 7:30 AM to 4 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571) 272-1722. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Christian Wilson, Ph.D.

Primary Examiner Art Unit 2891